Dover Microsystems CTO Featured Speaker at Embedded World 2019

Presentation with Antmicro explores increasingly critical role of Renode to address 'whole stack' co-design engineering challenges

WALTHAM, MA, February 20, 2019 – [Dover Microsystems](#) today announced CTO, Steve Milburn, with be a featured presenter at [Embedded World](#), February 26th-28th in Nuremberg, Germany. Together with Michael Gielda, Vice President of Business Development, Antmicro, Steven will present "Design Cycle Acceleration for Hardware/Software Co-Design with Renode," on Wednesday, February 27th at 12:00 p.m. The presentation, based on a new case study, highlights how open-source, extensible, debuggable and robust functional simulation has been critical to Dover's success and why it should become a standard part of all hardware and embedded system design and development efforts.

Dover's flagship cybersecurity product, [CoreGuard](#), involves both silicon IP, as well as runtime and toolchain modifications. Embedded systems designs' such as this that involve 'whole stack' co-design, present novel engineering challenges. Traditional hardware-led approaches do not work, as the software and toolchain need to co-evolve with the hardware. Complex tradeoffs around where in the stack to implement functionality and to optimize require hardware-software co-design. In the presentation, Milburn and Gielda will highlight how Dover is using the Renode open-source functional simulation framework to address these challenges and drastically reduce design cycle, rapidly explore architecture trade spaces, and engage in hardware and software co-design.

To meet up with Dover at Embedded World and schedule an in-person demo, please contact Sarah Murray, smurray@emergepr.com

What: [Embedded World 2019; "Design Cycle Acceleration for Hardware/Software Co-Design with Renode"](#)

When: Wednesday, February 27th at 12:00 p.m.

Where: Nuremberg Exhibition Centre, Nuremberg, Germany

About Dover Microsystems
Dover is the first company to bring real security, privacy, and safety enforcement to silicon. Dover's patented CoreGuard solution integrates with market-leading RISC

processors to protect against cyberattacks, flawed software, and safety violations. For more information about the company, please visit www.dovermicrosystems.com.

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