



Dover Microsystems Underscores Commitment to RISC-V Foundation

Founder & CEO, Jothy Rosenberg, Featured Presenter at Annual RISC-V Summit

WALTHAM, MA – November 27, 2018 – As a founding member of the RISC-V Foundation, [Dover Microsystems](#) is committed to helping enable a new era of processor innovation alongside like-minded hardware and software providers. In this spirit, Dover’s Founder and CEO, Jothy Rosenberg, will be a featured presenter at the [Annual RISC-V Summit](#), December 3-6 at the Santa Clara Convention Center in California. The four-day event features thought-provoking sessions to explore a wide range of topics including RISC-V ISA Architecture, Software and Silicon, Vectors and Security, and more.

“I am thrilled to participate in the Annual RISC-V Summit, the only forum dedicated to driving the development and adoption of the RISC-V ISA,” Rosenberg said. “As we continue to invest in our flagship product CoreGuard, and evangelize the critical need for computing security, we remain steadfast in our commitment to supporting the RISC-V Foundation to empower the next generation of processor innovation and ensure a safer, more secure world.”

In his RISC-V Summit keynote presentation, “[If We Get RISC-V Security Right, It Will Become the Dominant Processor in the \\$470B IoT Market](#),” Rosenberg will highlight the factors that make RISC-V an ideal platform for IoT security and will underline the three key innovations that can help protect a RISC-V core from network-based attacks. Rosenberg will also participate in a media roundtable focused on RISC-V security and be a featured speaker on an industry panel discussion, “RISC-V Security Ecosystem: Open for Business.” Joined by colleagues from Andes Technology, Google, Hex Five Security and Rambus, the panel will discuss the RISC-V security issues facing customers, highlight several commercial solutions to secure RISC-V and offer attendees insight into holistic test & verification best practices at the platform level.

Dover’s leadership team, including Chief Technology Officer, Steve Milburn, and Chief Scientist, Greg Sullivan, are recognized RISC-V ambassadors who serve as speakers at numerous workshops and events. In 2018 alone, Dover hosted several RISC-V meetups, including inaugural gatherings in Austin, TX and Boulder, CO, bringing community members together regionally to encourage collaboration and drive adoption of the RISC-V ISA. Additionally, Dover executives serve on both the RISC-V Security and Marketing Committees, helping to generate awareness and spearhead market education around the value of RISC-V’s clean, open source architecture.

Over the last year, Dover has expanded its partner ecosystem to include fellow RISC-V foundation members including [SiFive](#) and [Antmicro](#), further solidifying the company's commitment to partnering with organizations that are similarly dedicated to ensuring the security, safety, and privacy of embedded systems through processor innovation. Dover will continue to identify complementary solution providers among the RISC-V community to support its customers and ensure a more secure connected world.

For more information about the event, click [HERE](#).

About Dover Microsystems

Dover is the first company to bring real security, privacy, and safety enforcement to silicon. Dover's patented CoreGuard solution integrates with market-leading RISC processors to protect against cyberattacks, flawed software, and safety violations. For more information about the company, please visit www.dovermicrosystems.com.

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